

AGILENT TECHNOLOGIES, INC.
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2/11
PATENT APPLICATION 4/

ATTORNEY DOCKET NO. 10003432-1

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Francis Joseph

Serial No.: 09/989,667 ✓

Examiner: Donna K. Mason

Filing Date: Nov. 20, 2001

Group Art Unit: 2111

Title: SYSTEM-ON-CHIP ARCHITECTURE THAT UTILIZES FERAM AND RE-CONFIGURABLE
HARDWARE

COMMISSIONER FOR PATENTS
PO Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ (X) under 37 CFR 1.97(b), or
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- ☐ () under 37 CFR 1.97(c) together with *either* a:
 - ☐ () Statement under 37 CFR 1.97(e), or
 - ☐ () a \$180.00 Processing fee under 37 CFR 1.17(p), or
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ () under 37 CFR 1.97 (d) together with a:
 - ☐ () Statement under 37 CFR 1.97(e), *and*
 - ☐ () a \$180.00 processing fee under 37 CFR 1.17(p).
(Filed after final action or notice of allowance, whichever occurs first, but before payment of the issue fee)

X FEE AUTHORIZATION

Please charge to Deposit Account **50-1078** the sum of \$0.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account **50-1078** pursuant to 37 CFR 1.25.

X STATEMENT UNDER 37 CFR 1.97(e)

The undersigned certifies that:

- ☒ X Each item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the statement, or
No item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned after making reasonable inquiry, was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.

PRIOR APPLICATIONS

References identified with an asterisk (*) in the enclosed PTO Form 1449, were disclosed in prior Patent Application No. _____, filed _____, now U.S. Patent No. _____, and, as such copies thereof are not included pursuant to the provisions of 37 CFR 1.98(d).

() FOREIGN LANGUAGE DOCUMENTS

A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO form 1449, as presently understood by the individual(s) designated in 37 CFR 1.56(c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

I hereby certify that this correspondence is being deposited with the United States Postal Service as:

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I hereby certify that this paper is being facsimile
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Date of Facsimile:

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Respectfully submitted,

Francis Joseph

By *[Signature]*

Ian Hardeastle

Attorney/Agent for Applicant(s)

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Date: **July 27, 2004**

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(Use several sheets if necessary)

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09/989,667

Francis Joseph

Nov. 20, 2001

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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	TRANSLATION	
					YES	NO
		1 094 507 A2	04/25/01	EP		

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

		Radunovic, Bozidar, "An Overview of Advances in Reconfigurable Computing Systems", Proceedings of the 32nd Hawaii Intl. Conference on System Sciences, 1999, pp. 1-10.
		H. Toyoshima et al., "FeRAM Device and Circuit Technologies Fully Compatible with Advanced CMOS", IEEE 2001 Custom Integrated Circuits Conference, pp. 171-178.
		Lu, Guangming, "The MorphoSys Dynamically Reconfigurable System-On-Chip" Evolvable Hardware, 1999, 1-9.

DATE CONSIDERED

*** Copies of these references are not enclosed pursuant to 37 CFR 1.98(d). (See accompanying IDS)**

